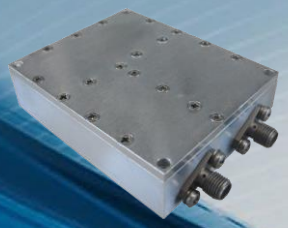
PHASE LOCKED CRO MODULE



ACPLCRO-DL Double Loop

Ed.01
23/09/10

ACORDE

ACPLCRO-DL is a multiplied frequency double loop phase-locked ceramic resonator oscillator designed to meet all requirements of the modern commercial and military telecommunication systems.

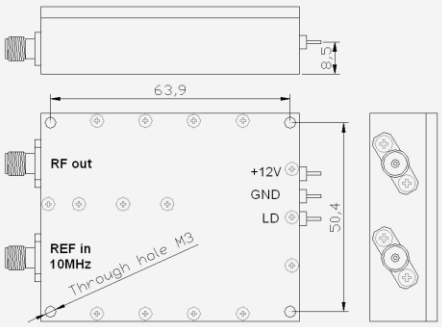
It was designed and tested to operate in extreme environmental conditions, low/high temperatures and stress. Beside excellent temperature stability, low phase noise and spectral purity, phase locked to 5 or 10MHz reference oscillator ACPLCRO has very low power consumption. Operating temperature range is -20ºC to +60ºC (-40ºC to +60ºC option is also available).

INPUT SPECIFICATIONS

Reference frequency	10 MHz (5 MHz as option)
Reference mode	External
Reference frequency level	0 dBm ± 3 dB

OUTPUT SPECIFICATIONS

Output frequency	4.5 – 6.0 GHz (C-band)
(Other frequency bands available upon request)	6.0 – 10.5 GHz (X-band)
	10.5 – 15.0 GHz (Ku-band)
Output power	13 dBm ±1dB
Output impedance	50 Ω
Output VSWR	< 1.5:1
Output power stability vs temperature	± 1.5 dB
Output phase noise @ 14.5 GHz:	
100 Hz	-80 dBc/Hz
1 kHz	-100 dBc/Hz
10 kHz	-105 dBc/Hz
100 kHz	-115 dBc/Hz
1 MHz	-135 dBc/Hz
Harmonics	≤ -50 dBc
Spurious	< -70 dBc



POWER SUPPLY

Input voltage	+ 12 Vdc
Current consumption	320 mA (typ.)

ENVIRONMENTAL SPECIFICATIONS

Storage temperature	-40 to +85ºC
Operating temperature	-20 to +60ºC (-40 to +60ºC as option)

MECHANICAL SPECIFICATIONS

Interfaces:	
Reference input:	SMA female
RF output:	SMA female
DC input:	7A feed thru
Lock Detect (0/+12V):	7A feed thru
Dimensions	56 (L) x 70 (W) x 18 (H) mm
Weight	150 g

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